Week1 assignments

1. A computer has 512k B cache memory and 2mb main memory. If the block size is 64 bytes then find subfield fo
   1. associative memory
   2. direct mapping
   3. set-associative mapping
2. how does cache memory increase the speed of processing ? Explain.
3. What will be the maximum capacity of a memory, which uses an address bus of size 8 bit?
4. What is the drawback of direct mapped catch? How it is resolved in set associative cache?
5. A computer has 1 KB 4-way set associative catch and 1 MB main memory. If the block size is 64 B, then in which catch set are the words (ABCDE)16 and (EDCBA)16 mapped?